



# Tecnoroll BMB s.r.l.

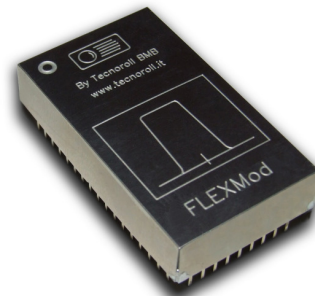
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## FLEXMod 3.0

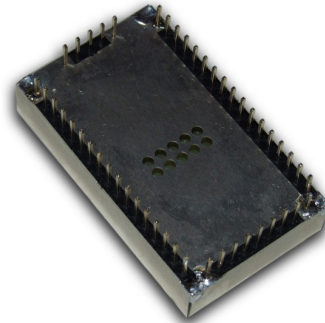
**Multi-Standard modulation platform.**

**Available either as a finished product  
 or as a hardware-only platform  
 for customer IP implementation.**

Front



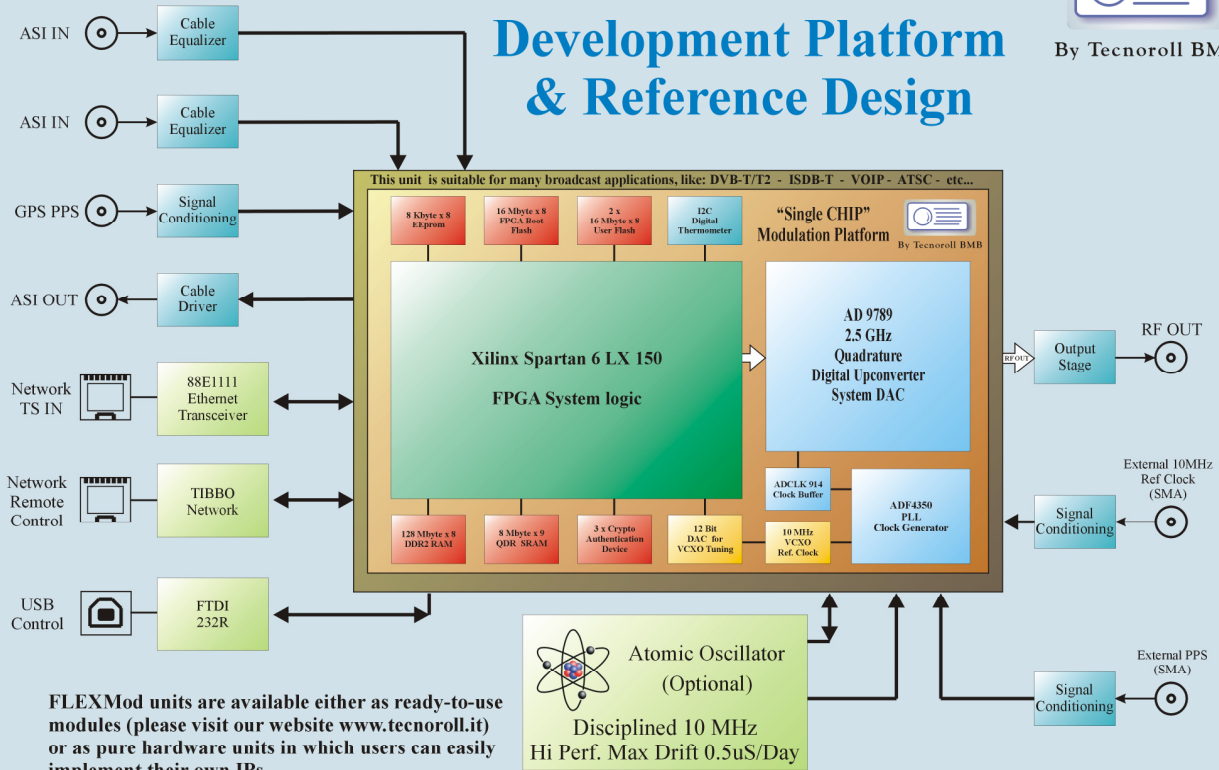
Back



## FLEXMod 3.0 Development Platform & Reference Design



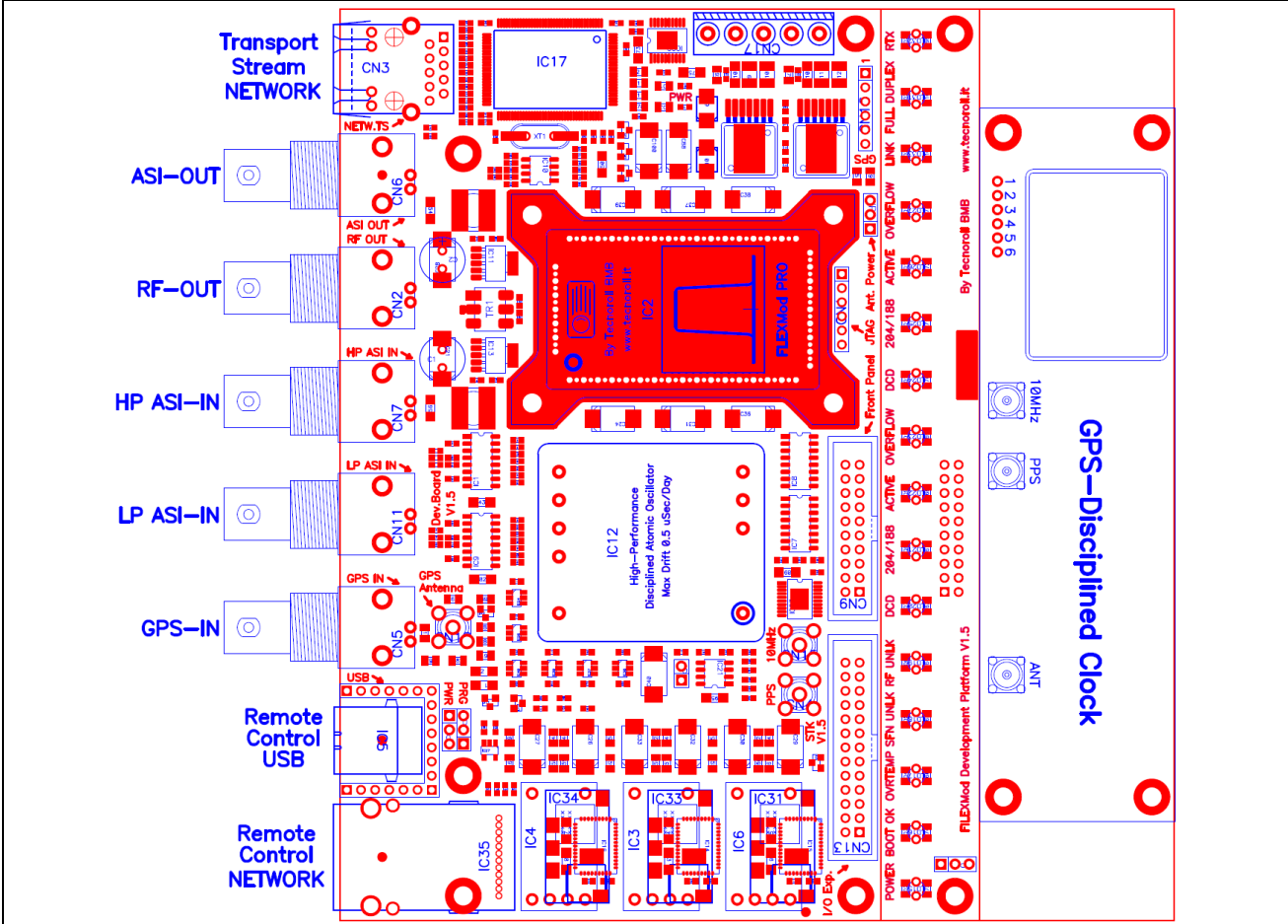
By Tecnoroll BMB



## General Specifications:

<b>System FPGA:</b>	SPARTAN 6 LX 150
<b>RF DAC Converter:</b>	ANALOG DEVICES AD9789 QDUC
<b>Clock Management:</b>	Internal PLL (ADF4350) to generate any DAC clock frequency
<b>Clock Reference:</b>	Internal (10MHz) or external reference clock capability
<b>Clock Tuning:</b>	12-Bit DAC to control the internal VCXO Clock Reference source
<b>IP Management:</b>	Up to 3 Crypto Authentication devices to manage 3 <sup>rd</sup> party IP Cores
<b>FPGA Boot Flash:</b>	One 128-Mbit (16 Mbytex8) serial flash
<b>User Flash:</b>	Two 128-Mbit (16 Mbytex8) serial flash for user applications & storage
<b>First RAM Bank:</b>	128 Mbyte x 8 DDRII for system processor & TS buffering
<b>Second RAM Bank:</b>	8 Mbyte x 9 QDR for Fast Random Access (Time & Frequency interleaving)
<b>Thermometer:</b>	Internal thermometer for system monitoring functions
<b>Dimensions:</b>	29 x 49.5 x 9.5 mm
<b>Weight:</b>	30 g

## Reference design:



## Distributed by:



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